

IDENTIFICATION SYSTEMS

REFERENCES

CLF 81/24 : standard board powered exclusively with 24VDC

GC 01 : board holder, to be ordered separately (refer to the corresponding data sheet).

DESCRIPTION

- Euro-card format electronic board (100 x 160 mm).
- Allows reading of electronic tags type OF(R) when it is connected to a reading head type ERA, ERO or TLEB 891.
- By addressing every byte in the tag in turn, all the tag data can be read: one code is delivered by eight parallel outputs at each operation.
- Multiplexable parallel port.

CONFIGURATION

| SWITCH | | | | version |
|--------|-----|-----|-----|----------|
| 1 | 2 | 3 | 4 | |
| Off | Off | Off | Off | standard |
| On | Off | Off | Off | 1221 |
| Off | On | Off | Off | 1255 |
| On | On | Off | Off | 1262 |

Version 1221 : Automatic initialisation of reading; last read code is stored.

Version 1255 : Automatic reading.

When no tag is detected, outputs are set to FF.

Read code and VAL are held for at least 100 ms.

Version 1262 : Automatic reading.

When no tag is detected, outputs are set to 0.

If LEC = 1, all the outputs go to 1 (outputs status test) thus disabling reading (PRE = 0, VAL = 0).

When LEC = 0, the board resumes normal operation.

CHARACTERISTICS

| GENERAL | Parameters | | MIN | NOM | MAX | Units |
|---------|-----------------|---|-------------------------|-----|-----|-------|
| | T _A | Ambient temperature | - 25 ¹⁾ + 70 | | | °C |
| | Weight | | 300 | | | g |
| | U _{CC} | Supply direct voltage (ripple included) | 21 | 24 | 29 | V |
| | I _{CC} | Supply current ²⁾ @ 24VDC | 30 | | | mA |

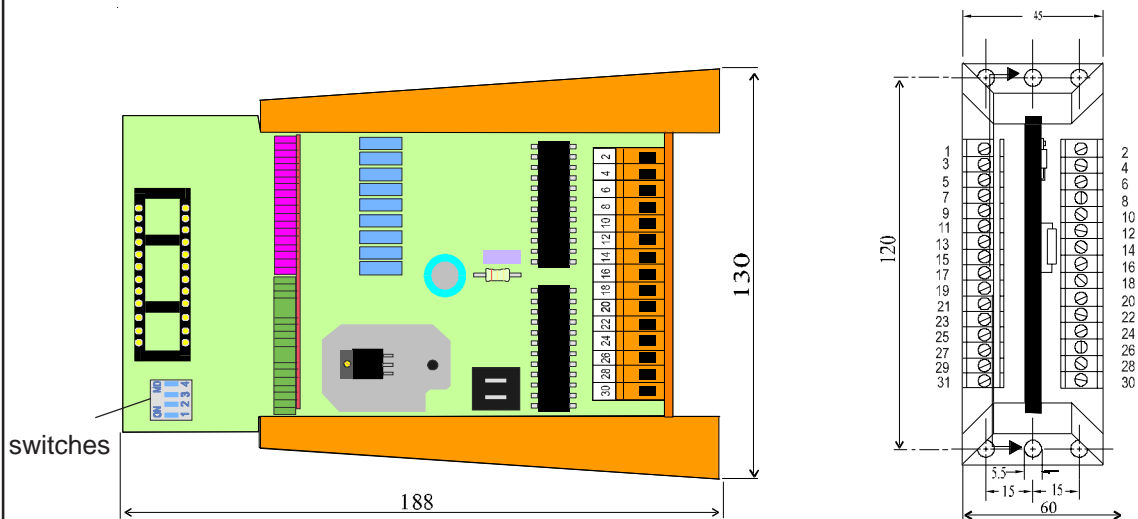
| PARALLEL OUTPUTS | I _O | Continuous output current | 200 | | | mA |
|---------------------|------------------------------|--|--|--|--|----|
| | V _{OH} | High-level output voltage @ I _O = 0 mA @ I _O = 200 mA | U _{CC} - 2.5 U _{CC} - 4.5 | | | V |
| | I _{OH} | High-impedance output leakage current | 500 | | | μA |
| | Protection against overloads | | yes | | | — |

| PARALL. INPUTS | Z _I | Input impedance | 2.5 | | | kΩ |
|-------------------|-----------------|--------------------------|-----|-----------------|--|----|
| | V _{IL} | Low-level input voltage | 0 | 10 | | V |
| | V _{IH} | High-level input voltage | 15 | U _{CC} | | V |

¹⁾ provided the board is condensation-free

²⁾ excluding consumption of the head and outputs

DIMENSIONS



CONNECTIONS

The electrical connections of the new CLF 81 is compatible with those of the former. However to comply with the ECM guidelines, it is recommended to connect as follows:

| LEDS colour | LEDS rank (from right to left) | Term. | Description | | Connection | |
|-------------------------------------|--------------------------------|-------|--|---------|--------------------------|---------------------|
| | | | | | Head with wiring chamber | Head with connector |
| R | 1 | 1 | Output to the head, to be connected to | - - -> | terminal E | pin 3 |
| | 2 | 2 | Bit 2 Echoes of | | | |
| | 3 | 3 | Bit 1 reading | | | |
| | 4 | 4 | Bit 0 address | | | |
| E | 5 | 5 | Output bit 7 | | | |
| | 6 | 6 | Output bit 6 | | | |
| | 7 | 7 | Output bit 5 | | | |
| | 8 | 8 | Output bit 4 | | | |
| D | 9 | 9 | Output bit 3 | | | |
| | 10 | 10 | Output bit 2 | | | |
| | 11 | 11 | Output bit 1 | | | |
| | 12 | 12 | Output bit 0 | | | |
| | 13 | 13 | Output VAL (enable) | | | |
| | 14 | 14 | Output PRE (tag presence) | | | |
| | | 15 | +V head supply | - - - - | terminal V | pin 1 |
| | | 16 | 0V head supply | | | |
| | | 17-21 | reservation | - - - - | terminal O | pin 4 |
| G | 6 | 22 | Bit 2 Selection of | - - -> | terminal S | pin 2 |
| R | 7 | 23 | Bit 1 reading | | | |
| E | 8 | 24 | Bit 0 address | | | |
| E | 9 | 25 | Input LEC (reading) | | | |
| | 10 | 26 | Input SBA (CLF select) | | | |
| N | 11 | 27 | Input from the head, to be connected to | | | |
| jumper 26 to 30 if there is one CLF | | 28 | Ground, to be connected to | shield | | |
| | | 29 | reservation | | | |
| | | 30 | + Ucc | | | |
| | | 31 | 0V power | | | |